bi−1 <u>.</u> -72, .:J bi+1 出 CPASel 7, 0 W 38

Fig. 1

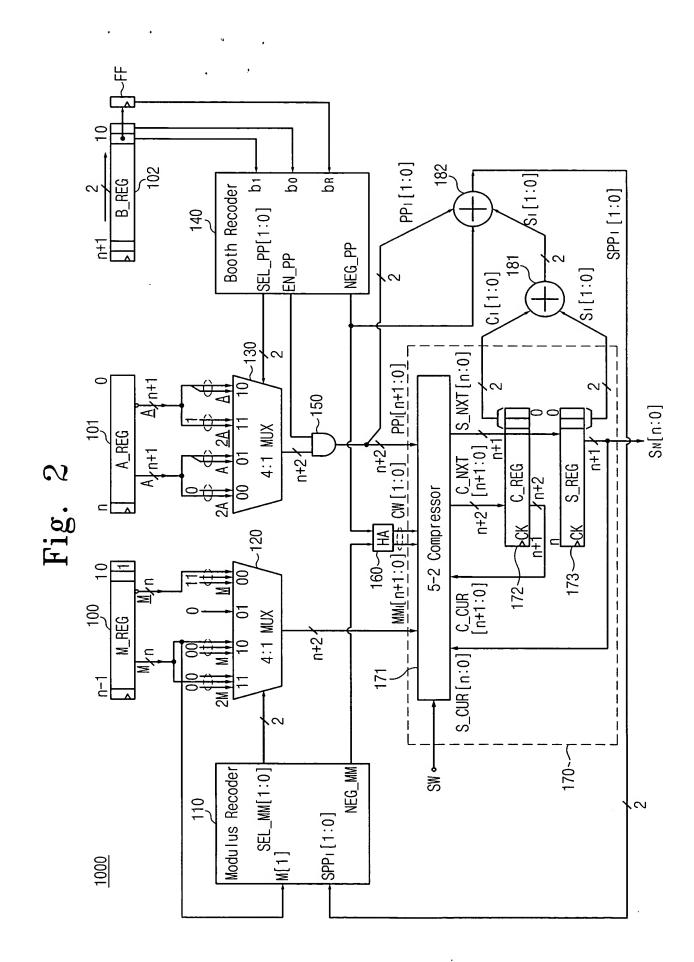


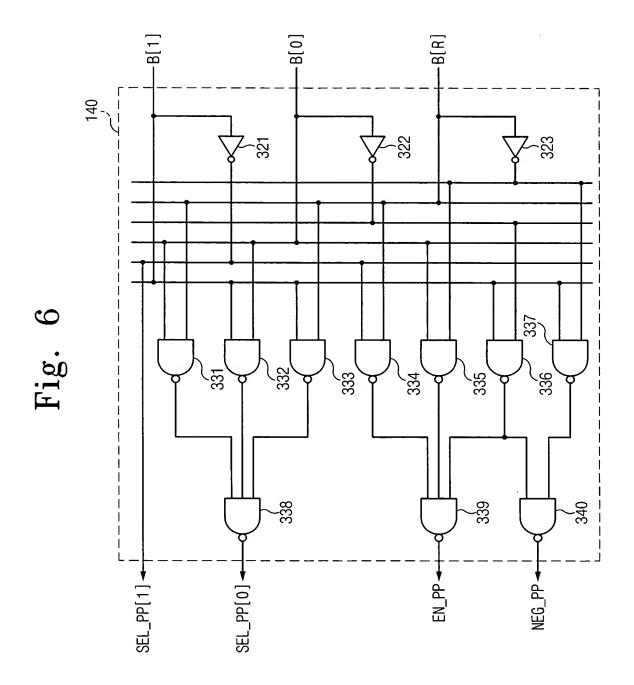
Fig. 3

Inputs of	Inputs of Modulus Recoder(110)	oder (110)	Outputs of Mod	Outputs of Modulus Recoder(110)	Selected
SPP <sub>1</sub> [1]	SPP <sub>1</sub> [0]	M[1]	SEL_MM[1:0]	NEG_MM	MM <sub> </sub> [n+1:0]
0	0	0	11	0	0
0	0	1	11	0	0
0	1	0	01	1	W
0	-	1	00	0	W
1	0	0	10	0	2M
1	0	1	10	0	ZM .
1	1	0	00	0	W
-	-	1	01	-	W

- SPP, [1] -SPP<sub>1</sub>[0] -M[1] 9-1 Z\_88 Z~<u>E</u> 303 Fig. 4 314 315 312 311 313 316 317 SEL\_MM[0]~ SEL\_MM[1]

Fig. 5

Selected	PP <sub>1</sub> [n+1:0]	0	A	А	2A	<u>2A</u>	Ā	Ā	0
er (140)	NEG_PP	0	0	0	0	1	1	1	0
Outputs of Booth Recoder(140)	EN_PP	0	1		1	1	1	1	0
Outputs.	SEL_PP[1:0]	Don't care	00	00	10	11	01	01	Don't care
oder (140)	b <sub>R</sub>	0	-	0	-	0	-	0	1
nputs of Booth Recoder(140)	0q	0	0	-	<del>-</del>	0	0	-	-
Inputs of	b <sub>1</sub>	0	0	0	0	-	-		-



~CW,[0] ~MM,[0] ~CW,[0] CO<sub>1</sub> S<sub>1</sub>[0] C<sub>1</sub>[0] 202 CIS ~CW₁[1] ~WW¹[1] S<sub>[1]</sub> රි S S CI<sub>2</sub> CI<sub>1</sub> •PP<sub>[[2]</sub> -\S<sub>1</sub>[2] MM<sub>[2]</sub> CI<sub>1</sub> [u]<sup>1</sup>dd•  $S_{l}[n]$ -('I'] [u]|MM~ HOP<sub>n+1</sub> C1<sub>2</sub> [ l+n] | qq° S<sub>I</sub>[n] C<sub>I</sub>[n+1] [ +u] | MM • 8 202 S<sub>i</sub>[n] C<sub>1</sub>[n+1] 170a 205 171a~

Fig. 8

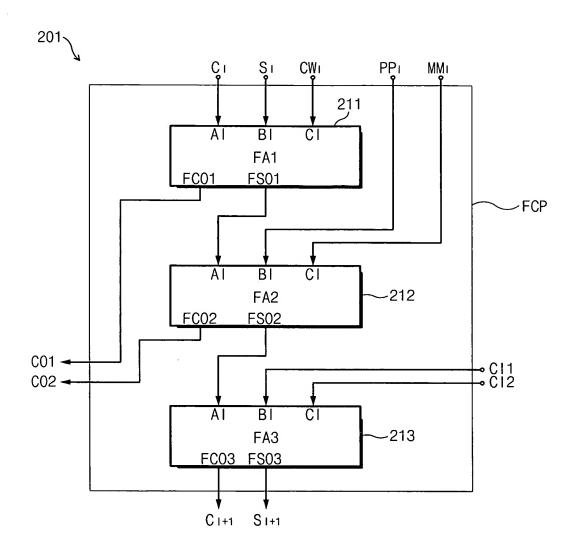
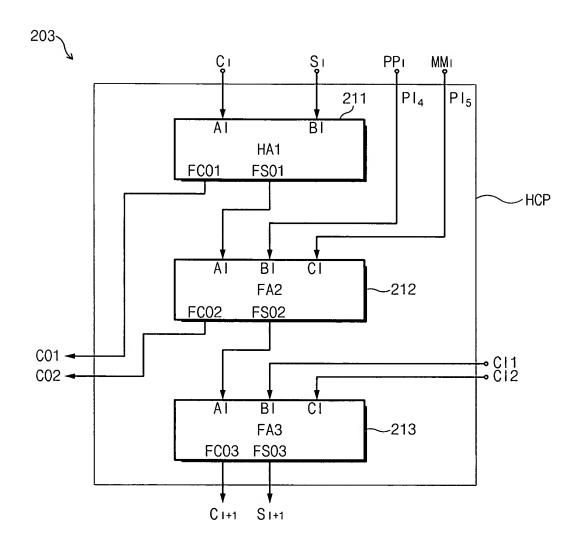
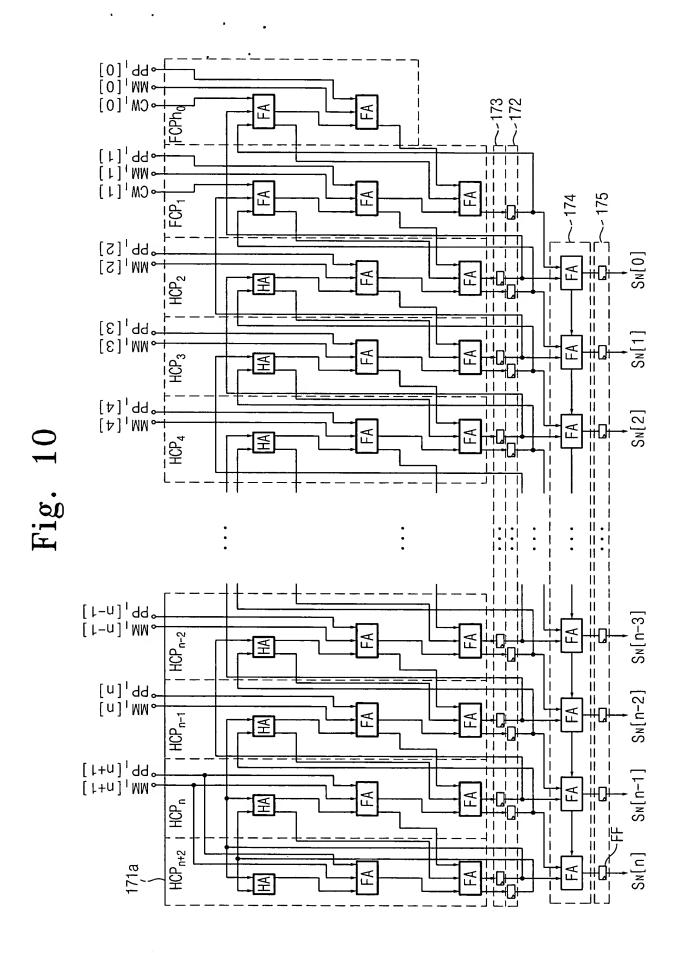
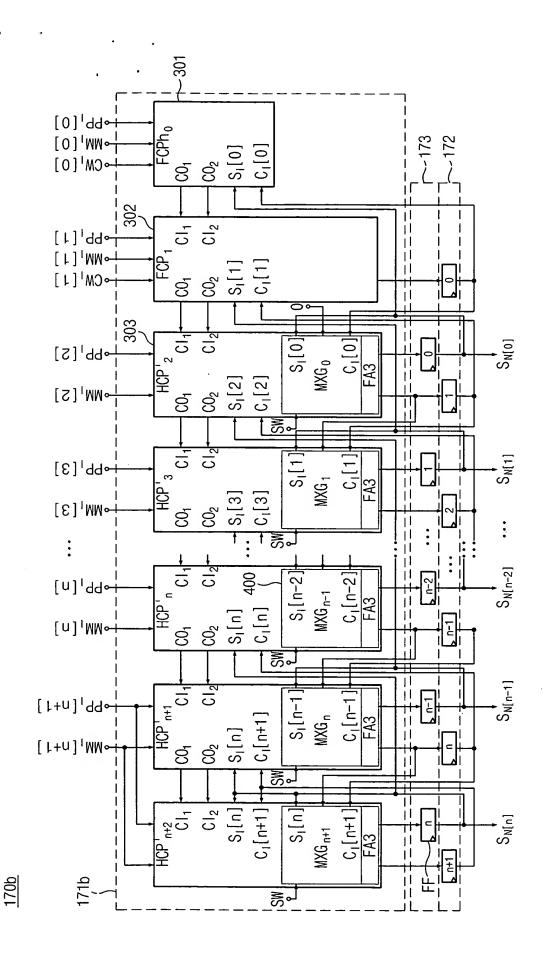


Fig. 9



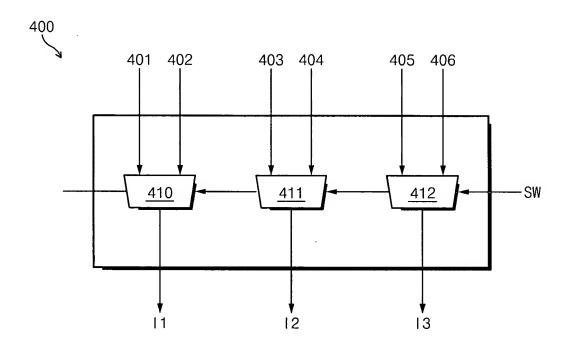




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Fig. 12



~bb'[0] ~WW'[0] ~CM'[0] MS ₽ FA ~ bb [1] ~ CM [1] ~ CM [1] [2]<sup>|</sup>dd。 SN[0] [8] MM of [3] S<sub>N</sub>[1] [4] MM 1-4 [4] 4q 1-4 SN[2] Fig. [r-n], MM。 [r-n], qq° SN[n-3][n]<sub>|</sub>MM ol [n]<sub>|</sub>qq ol | S<sub>N</sub>[n-2] FΑ [ h+n ] MM <u>| F</u> [ h+n ] qq • <del>| - - -</del>  $S_N[n-1]$ SN[n] 170b

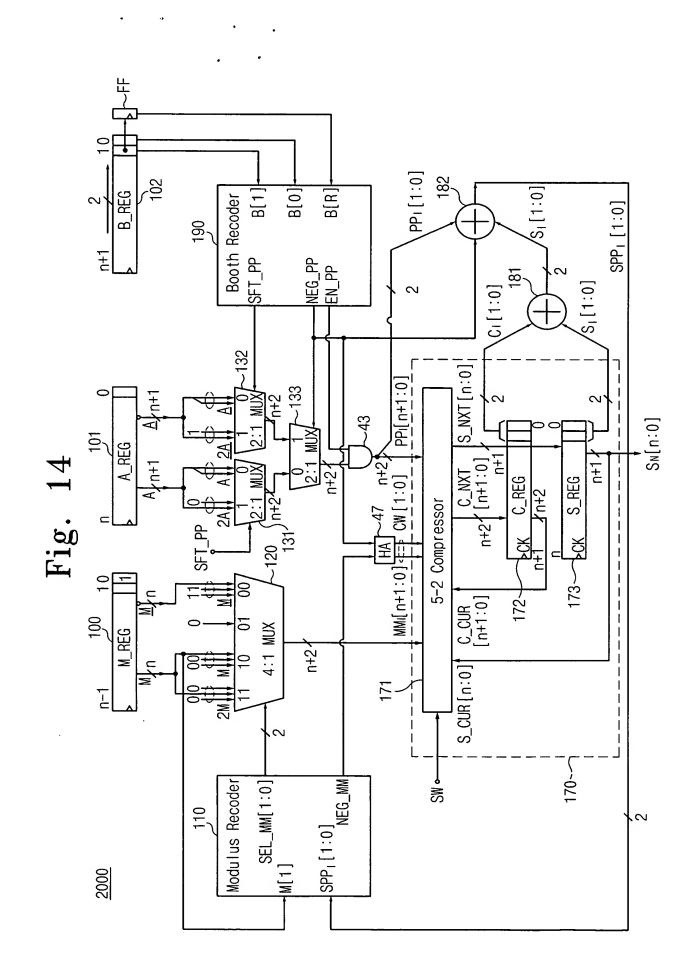


Fig. 15

nputs of Booth Recoder(190)	Sode	er (190)	Outpu	Outputs of Booth Recoder(190)	der (190)	Selected
B[0] B[R] SFT_PP		SFT_F	<u>م</u> ـ	EN_PP	NEG_PP	PP <sub>1</sub> [n+1:0]
0 0 0	0 0	0		0	0	0
0 1 0	1 0	0		ļ	0	А
1 0 0	0 0	0		ļ	0	A
1 1	1	l		Į į	0	2A
0 0 1	0 1	1		-	1	<u>2A</u>
0 1 0	1 0	0		1	1	Ā
1 0 0	0 0	0		1	1	Ā
1 1 0	1 0	0		0	0	0

-8[1] -B[0] -B[R] 95. 402 40, 403 Fig. 16 418 419 NEG\_PP ▲ EN\_PP

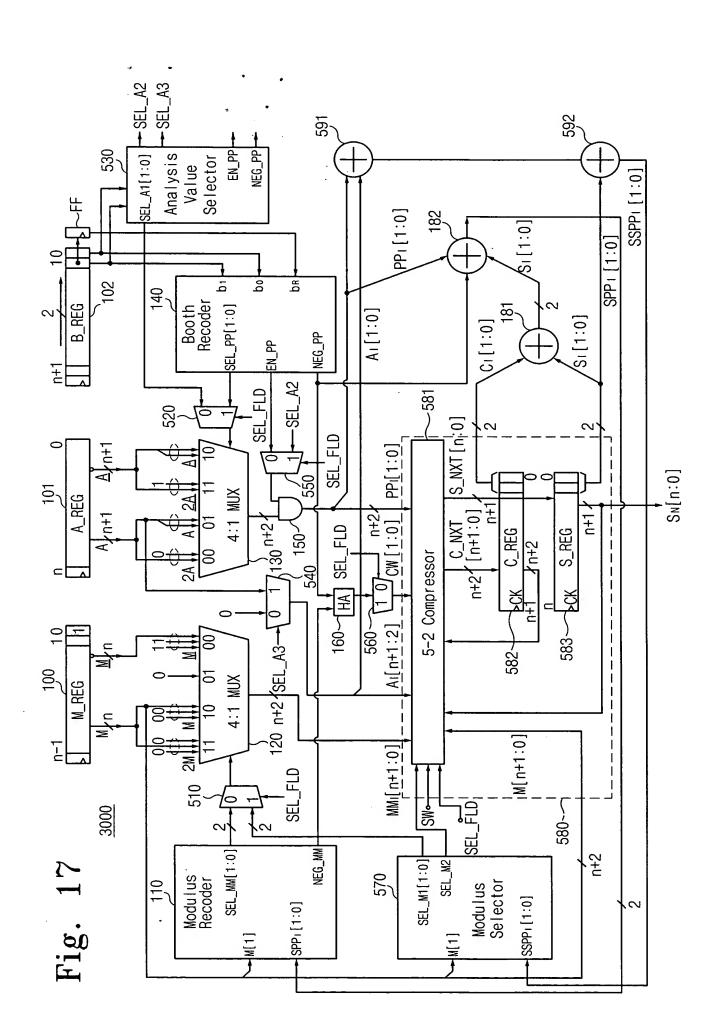


Fig. 18

Inputs of Value Sele	Inputs of Analysis Value Selector(530)	Outputs of A	Outputs of Analysis Value Selector(530)	elector(530)	Selected First Analysis Value	Se I Ana
B[0]	8[1]	SEL_A1[1:0]	SEL_A2	SEL_A3	PP <sub>1</sub> [n+1:0]	A <sub>1</sub> [n+1:0]
0	.0	Don't care	0	0	0	0
0	-	. 01	1	0	А	0
<del></del>	0	00	1	0	2A	0
·-	<del></del>	00	ļ	1	2A	А

Fig. 19

Inputs of Modulus Selector(570)	Modulus r(570)	ď	Outputs of Modu Selector(570)	Outputs of Modulus Selector(570)	Selected First Analysis Value	Selected First Selected Second Analysis Value Analysis Value
SSPP, [1:0]	M[1]		SEL_M1[1:0]	SEL_M2	MM,[n+1:0]	M <sub>1</sub> [n+1:0]
00	0	00	01	0	0	0
01	0	01	10	0	W	0
10	0	10	11	0	ZM	0
11	0	11	11	1	2M	W
00	1	00	10	0	0	0
10	1	11	. 11	1	2M	W
10	1	10	11	0	2M	0
11	-	01	10	0	M	0

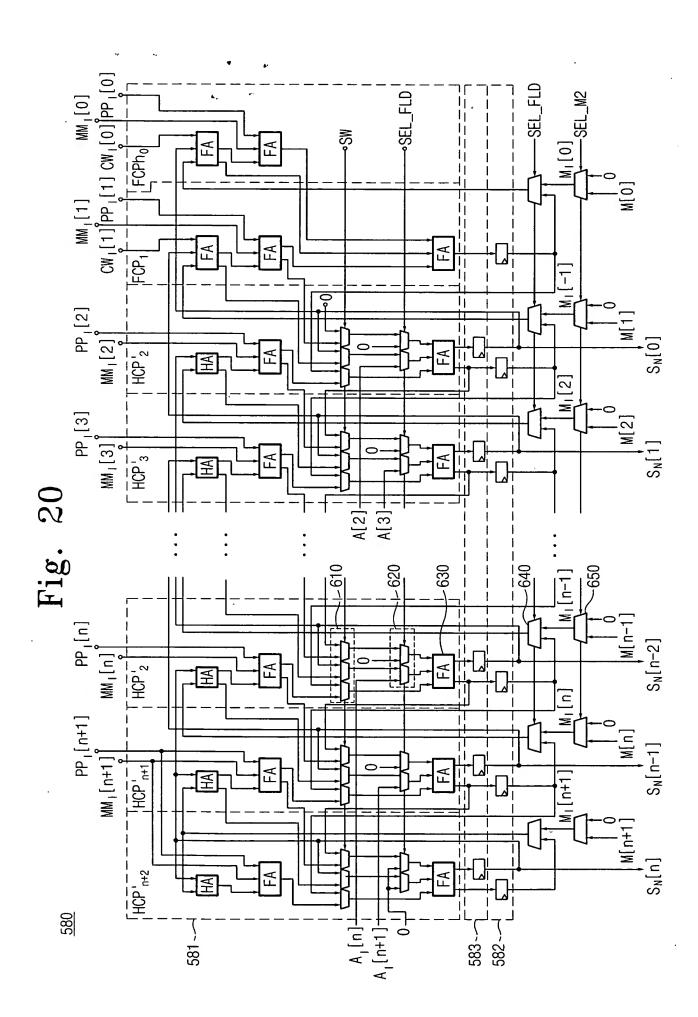


Fig. 21

